## **PATENT**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Angshuman Bezbaruah; Anand A. Kekre; Niranjan S. Pendharkar

Assignee: VERITAS Operating Corporation

Title: FAST FAILOVER WITH MULTIPLE SECONDARY NODES

Application No.: 10/728,027 Filing Date: December 4, 2003

Examiner: Gary W. Cigiel Group Art Unit: 2661

Docket No.: VRT0090P1US Confirmation No.: 8352

Austin, Texas September 26, 2007

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## **RESPONSE TO NON-FINAL OFFICE ACTION**

## Dear Sir:

This paper is responsive to the Office Action dated May 31, 2007, having a shortened statutory period expiring on August 31, 2007. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

No Amendments to the Specification are presented in this paper.

No Amendments to the Drawings are presented in this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

- 1 -

Remarks/Arguments begin on page 8 of this paper.

Application No.: 10/728,027